	Search Text
1	(bug$5 error$4) and fpga and (on adj chip soc)
2	(bug$5 error$4) and fpga and (on adj chip soc) and bus
3	(bug$5 error$4) and fpga and (on adj chip soc) and (bus interfacce)
4	(bug$5 error$4) and fpga and (on adj chip soc) and (bus interface)
5	(bug$5 error$4) and fpga and (on adj chip soc) and (interface)
6	(bug$5 error$4) and fpga and (on adj chip soc) and (bus interface) and diagnos$5
7	(bug$5 error$4) and fpga and (on adj chip soc) and (bus interface) and bus near3 protocol
8	(bug$5 error$4) and fpga and (on adj chip soc) and (bus interface) and register
9	(bug$5 error$4) and fpga and (on adj chip soc) and (mode)
10	(bug$5 error$4) and fpga and (on adj chip soc) and single adj mode and step adj mode
11	(bug$5 error$4) and fpga and bus adj protocol
12	(bug$5 error$4) and fpga and bus adj protocol and (on adj chip soc)